



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Horton et al.

Serial No.: 10/604205

Group Art Unit: Unknown

Filing Date: 7-1-03

Examiner: Unknown

For: CIRCUIT AND METHOD FOR PIPELINED INSERTION

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner. This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicants are aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 09-0456.

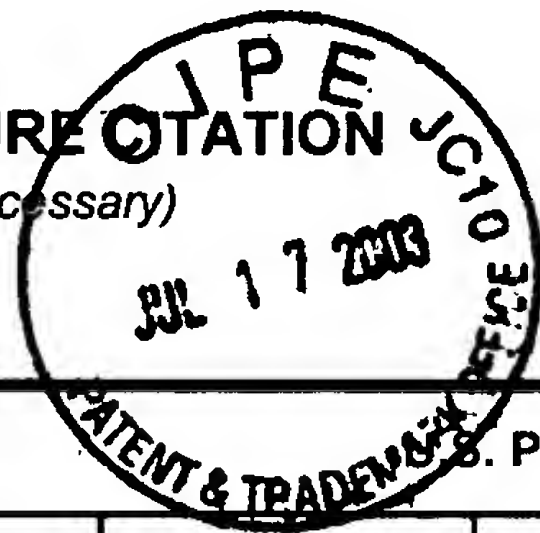
Respectfully submitted,

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



ATTY DOCKET NO.
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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		International Business Machines Corporation, "Hybrid Transistional Coding of Wide On-Chip Busses", Research Disclosure, Nov. 2001, pp. 1958-1959.
		Nalamalpu et al., "Boosters For Driving Long Onchip Interconnects-Design Issues, Interconnect Synthesis, and Comparison with Repeaters", IEEE Xplore, Vol. 21, No. 1, January 2002, pp. 50-62.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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A circular black ink stamp is located in the upper right quadrant of the page. The text "OFFICE" is curved along the top inner edge, and "JUL 17 2003" is stamped in the center. The words "PATENT & TRADEMARK" are curved along the bottom inner edge. The stamp is partially overlapping the "OFFICE" text and the "PATENT & TRADEMARK" text.

PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			Yamada et al., "A Novel Approach to Realizing Flexible Transport Systems Using Reconfigurable Hardware", IEEE, Vol. 1, November 1995, pp. 67-71.

EXAMINER

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